Abstract

A liquid crystal display device with a multi-timing controller that generates a timing signal according to an individual display standard from a control signal according to various display standards to drive the liquid crystal display device. In the device, a liquid crystal display panel has a display standard corresponding to an arranged pixel. An interface receives a data inputted from the exterior thereof and a control signal corresponding to the display standard. A timing controller latches and outputs a data inputted from the interface, and generates and outputs timing signals for driving the liquid crystal display panel from the control signal. A driving circuit receives the timing signals from the timing controller to display a picture corresponding to the data on the liquid crystal display panel. In the timing controller, a display standard set part sets one display standard in response to a plurality of display standards and generates a setting signal corresponding to the display standard. A selector has each timing generation information according the plurality of timing standards and outputs a timing information corresponding to the set signal. A timing generator receives the timing information to generate and output the timing signals from the control signal.